

Electronic Acknowledgement Receipt

EFS ID:	2916833
Application Number:	10699707
International Application Number:	
Confirmation Number:	3525
Title of Invention:	Reconfigurable chip level equalizer architecture
First Named Inventor/Applicant Name:	Antonio F. Mondragon-Torres
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Fee Worksheet (PTO-06)	FeeTrl.pdf	374872 a89662d3183c2d092caa717e65d78387f816acb4	no	1

Warnings:

Information:

2	Extension of Time	Extension_1mo.pdf	148095 0dbb0c6103fa6b030bf4e566564c9595013a8ae8	no	1
Warnings:					
Information:					
3		ROA_10-01-07.pdf	122186 f58f0d9c430d6bc4ddff435b6958f306497fe40	yes	8
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment - After Non-Final Rejection		1	1	
	Claims		2	6	
	Applicant Arguments/Remarks Made in an Amendment		7	8	
Warnings:					
Information:					
Total Files Size (in bytes):			645153		
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